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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. MI22-898	SERIAL NO. 09/059,644		
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Pai-Hung Pan			
				FILING DATE April 13, 1998	GROUP 2822		
U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass		
	AA 5,552,329	9-3-96	Kim et al.				
	AB						
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	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
M	AR		WOLF, Ph.D., Stanley et al., <u>Silicon Processing for The VLSI Era</u> , Vol. I, Lattice Press, pp. 216-218, (1986)				
M	AS		WOLF, Ph.D., Stanley et al., <u>Silicon Processing for The VLSI Era</u> , Vol. II, Lattice Press, pp. 66 & 67 (ended) (1990)				
	AT						
EXAMINER	Michael Trinh			DATE CONSIDERED		11/26/01	
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